



6674464

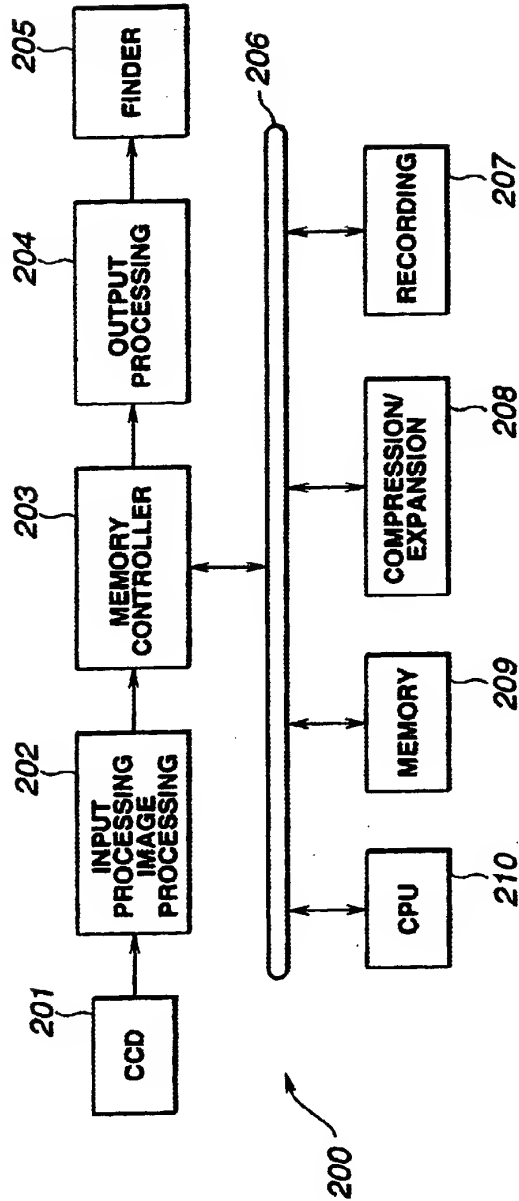


FIG.1
(PRIOR ART)

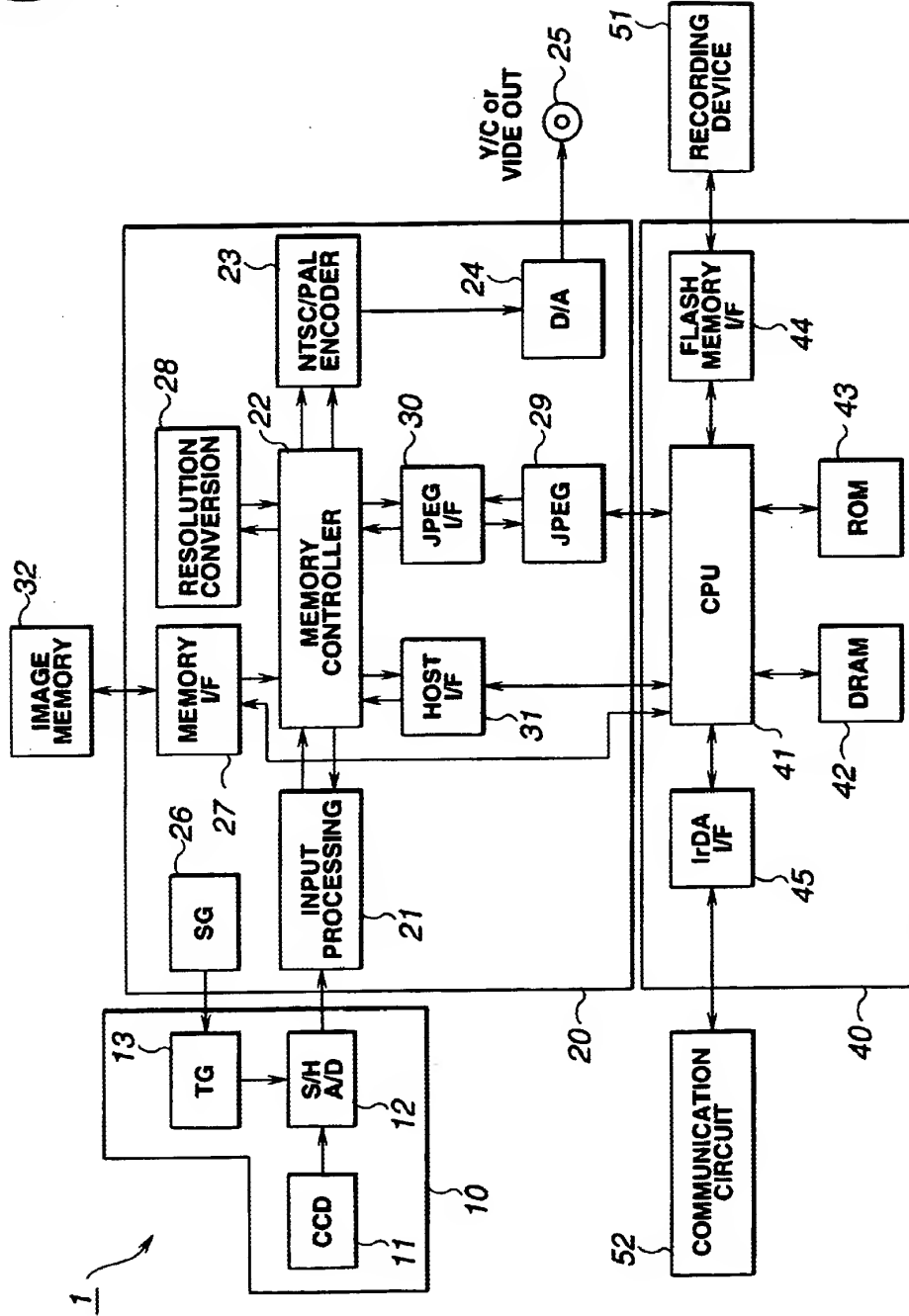


FIG.2

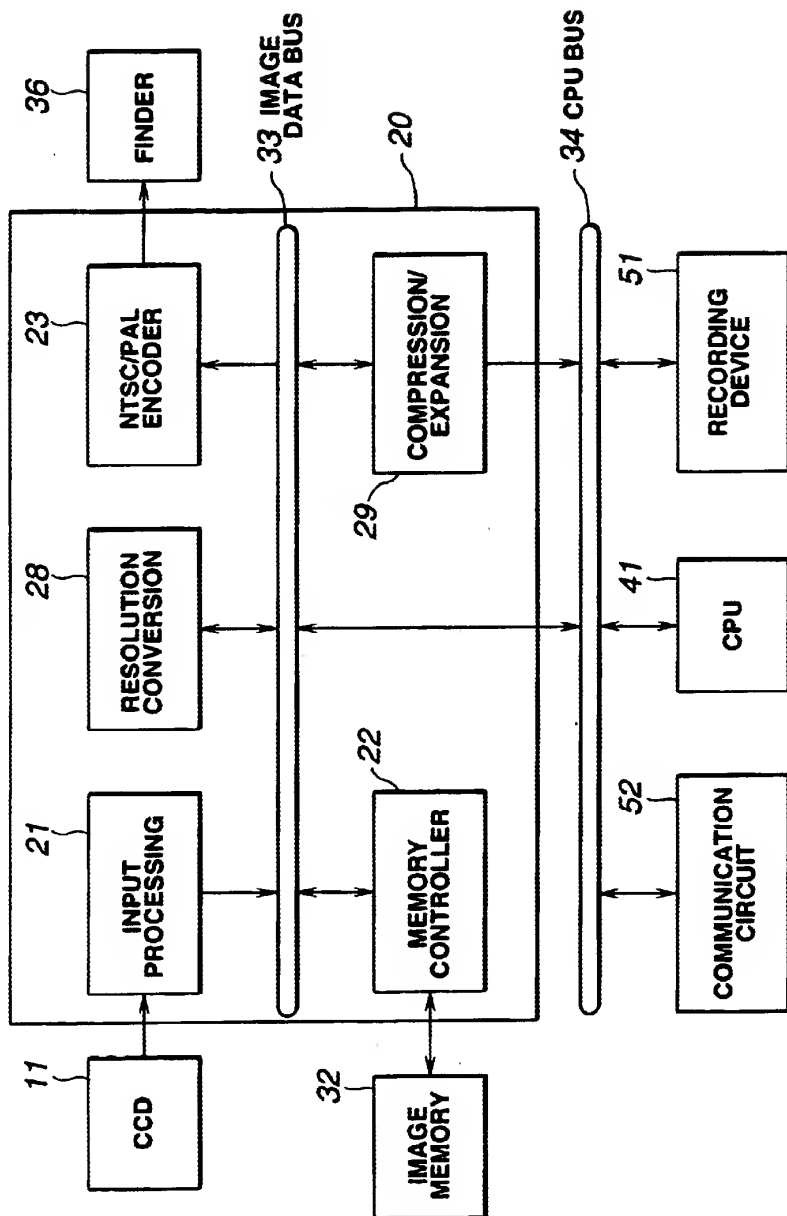


FIG.3

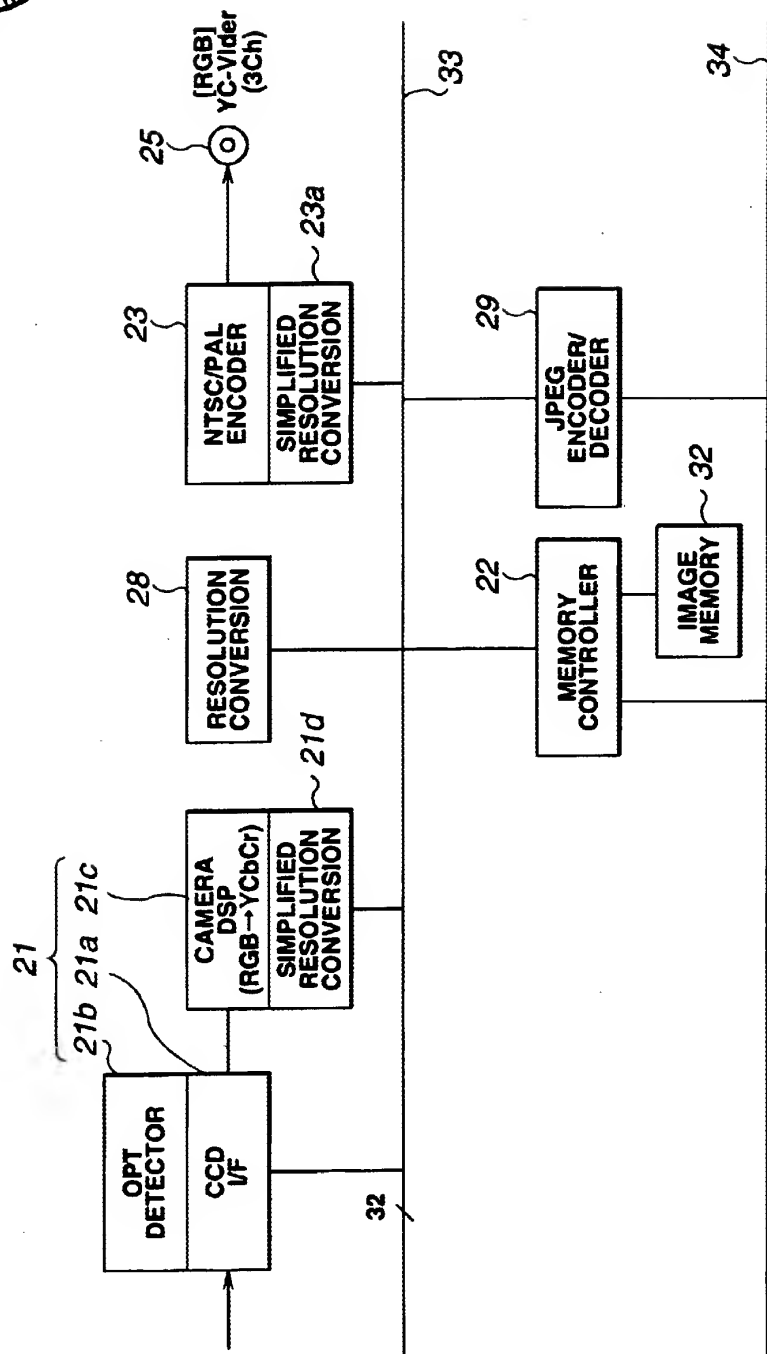


FIG.4

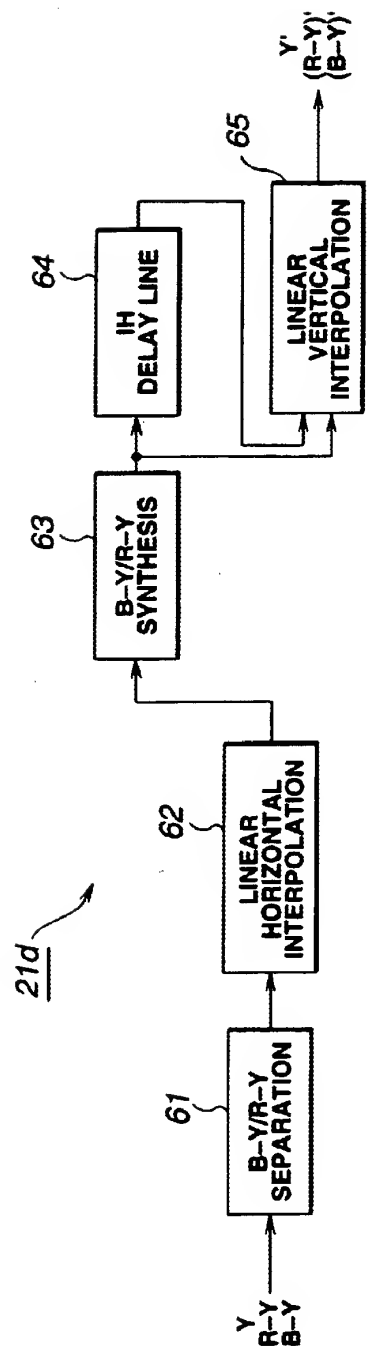


FIG.5

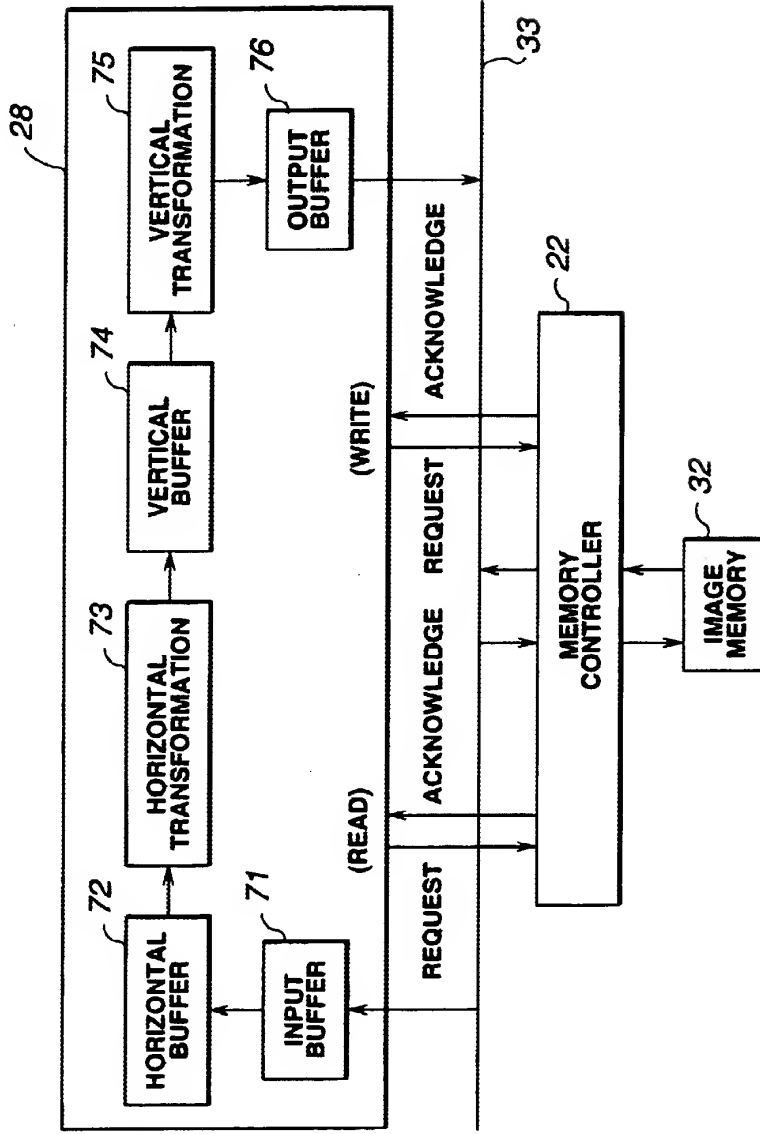


FIG.6

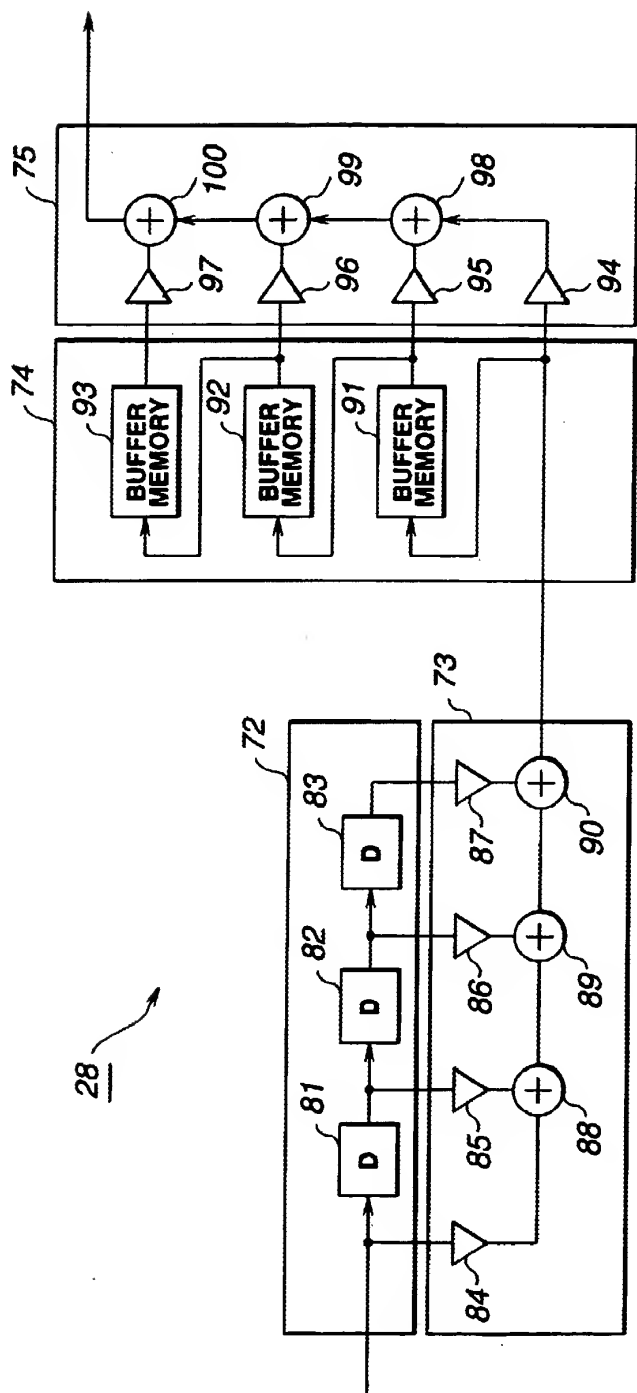


FIG. 7

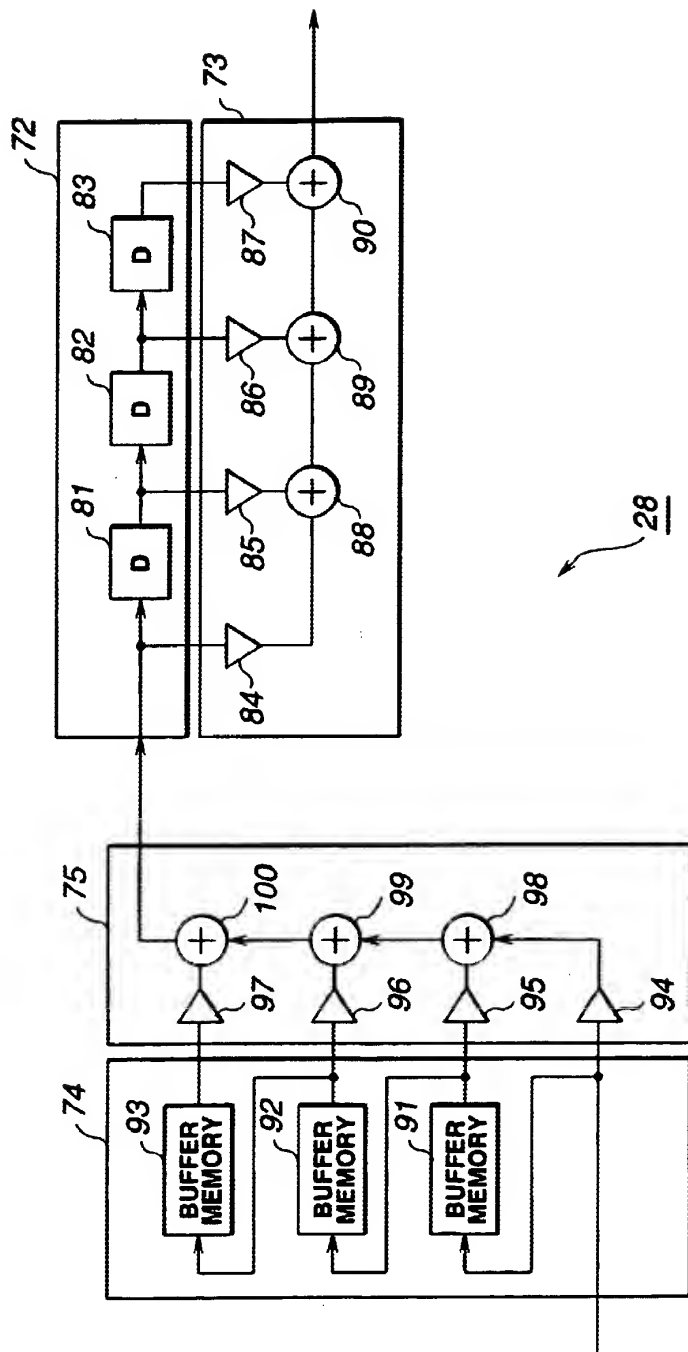


FIG. 8



Inventor(s): Yoichi MIZUTANI et al.
Invention: Imaging Apparatus for ... Image Data
Serial No.: 09/354,476

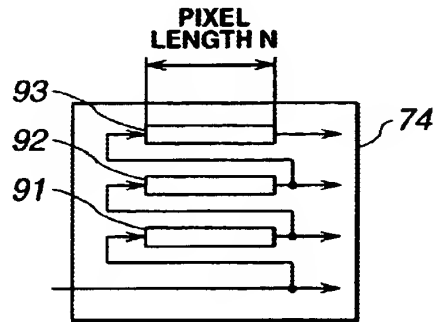


FIG. 9

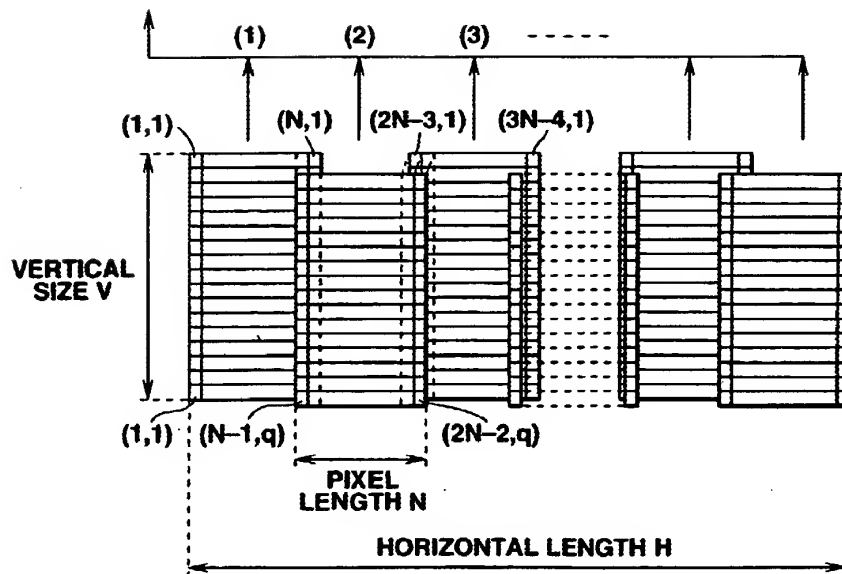


FIG. 10



Inventor: Yoichi MIZUTANI et al.
 Invention: Imaging Apparatus for ... Image Data
 Serial No.: 09/354,476

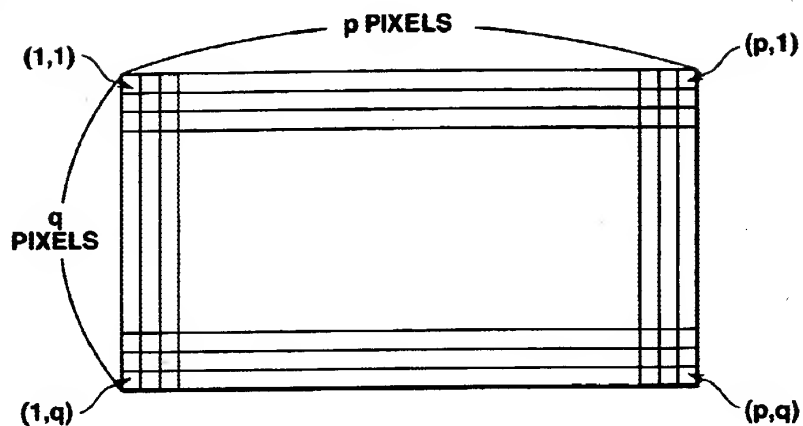


FIG.11

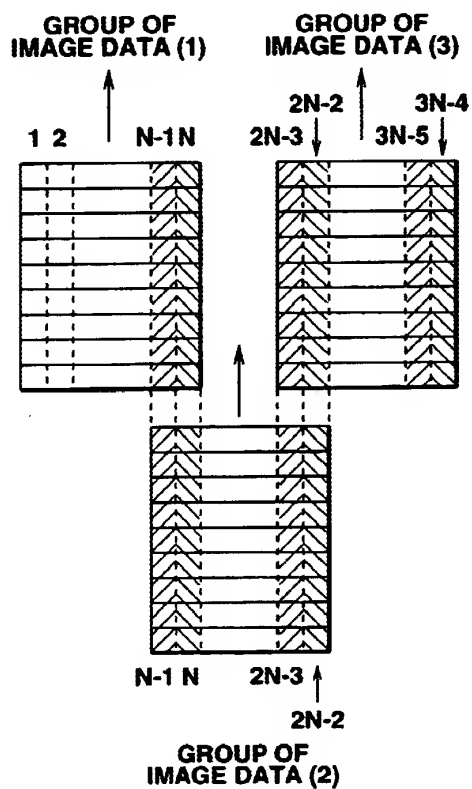


FIG.12



Inventor: Yoichi MIZUTANI et al.
Invention: Imaging Apparatus for ... Image Data
Serial No.: 09/354,476

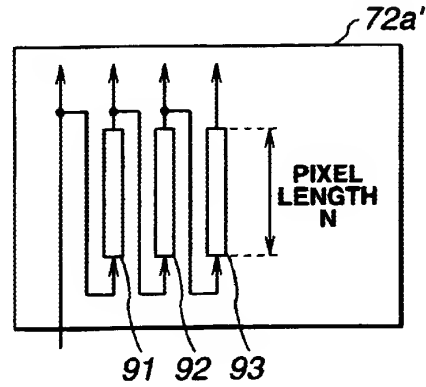


FIG.13

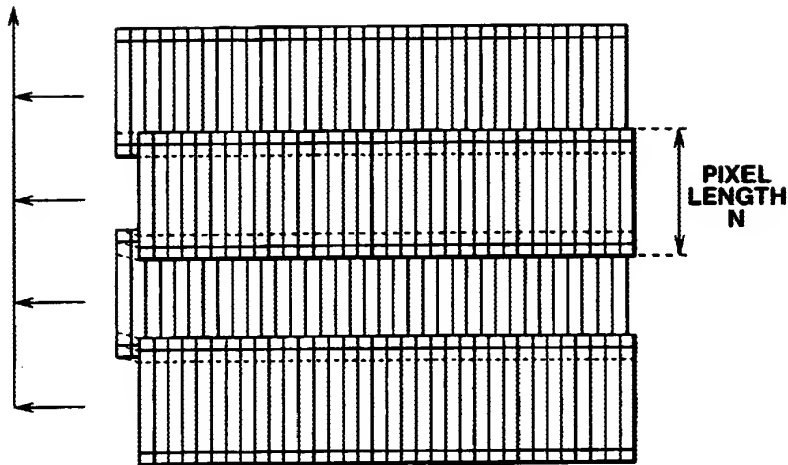


FIG.14



Inventor(s): Naichi MIZUTANI et al.
Invention: Imaging Apparatus for ... Image Data
Serial No.: 09/354,476

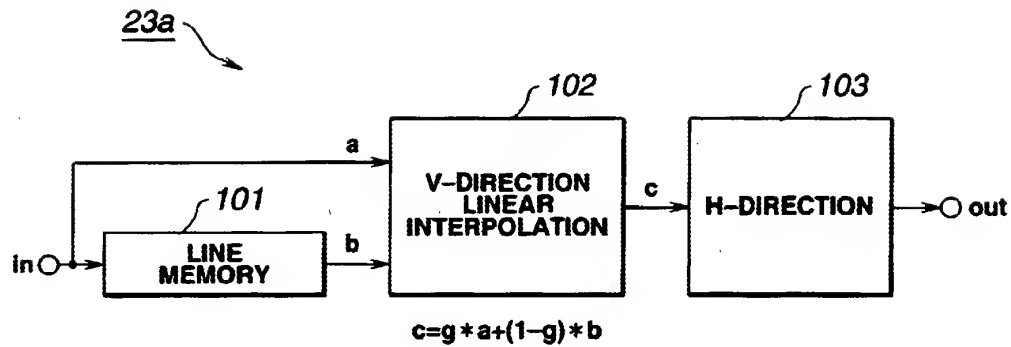


FIG.15

FIG. 16A CCLK_{12.27MHz}

FIG. 16B $\xrightarrow{\text{CD1F}}$ CAMDSP

[illegible][illegible][illegible]

FIG. 16F

IBUS →
DRAM →
DRAM →
CAMDSP →

C → D
D → N
D → N C → D D → O
D → N
C → D
D → N
D → O

DRAM (16bit)